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				Application Number	***
				Filing Date	Herewith
				First Named Inventor:	William G. Auld
				Group Art Unit	***
				Examiner Name	***
Sheet	1	of	1	Attorney Docket Number	42P17019

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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UA		BARROSO, L.A. et al., "Piranha: a scalable architecture based on single-chip multiprocessing", Proceedings of the 27 <sup>th</sup> Annual International Symposium on Computer Architecture, Vancouver, BC, Canada, pp. 282-293 (June 10-14, 2000).	

Examiner Signature	Kevin L Ellis	Date Considered	10/27/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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